

10/046077
JC903
01/16/02
U.S. PTO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hiroshi INAGAWA et al.

Appln. No.:

Filed: Herewith

For: INSULATED GATE TYPE SEMICONDUCTOR DEVICE AND METHOD FOR
FABRICATING THE SAME

* * *

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

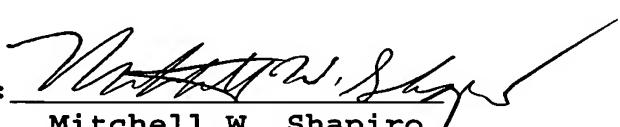
Pursuant to 37 C.F.R. § 1.56, and without any assertion
as to materiality or prior art effect, the documents listed
on the attached Form PTO-1449 are hereby cited.

The documents on the attached List are cited in the
specification, on pages 1 and 3, and their relevance is
indicated therein.

Respectfully submitted,

MWS:jab

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 903-9000

By: 
Mitchell W. Shapiro
Reg. No. 31,568

January 16, 2002